

Value Added Courses: 2018-2019

<b>Date(s)</b>	<b>VAC Course Name</b>	<b>No. of students Benefitted</b>	<b>Resource Persons</b>
28-12-2018 to 29-12- 2018	Value Added Course on "Digital IC Design using Cadence EDA Tolls"	20	Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi
08-02-2019 to 09-02- 2019	Value Added Course on "Custom Analog IC Design using Cadence EDA Tolls"	26	Dr.K.N.Vijeyakumar Dr.C.Kalamani Ms.S.Kalaiselvi